



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Peter L. Fu, et al.

Application No.: 10/071,014

Filed: February 7, 2002

For: DIRTY DATA PROTECTION FOR
CACHE MEMORIES

Date of Allowance: December 15, 2005

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Art Unit: 2187

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INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed).

Prior Art Summary:

Prior art UltraSPARC – II platforms from Sun Microsystems, Inc. of Santa Clara, California employ a software scrubber scheme for their external caches such that erroneous but clean (unmodified) lines are simply invalidated, since shared memory has good copies. In the prior art UltraSPARC – III from Sun Microsystems, Inc. of Santa Clara, California, there is hardware to do the same.

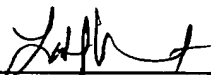
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Respectfully submitted,

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Dated: March 15, 2006



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